

**DECLARATION FOR PATENT APPLICATION  
AND POWER OF ATTORNEY**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of subject matter (process, machine, manufacture, or composition of matter, or an improvement thereof) which is claimed and for which a patent is sought by way of the application entitled

**CIRCUIT OPTIMIZATION FOR MINIMUM PATH TIMING VIOLATIONS**

- which (check)  is attached hereto.  
 and is amended by the Preliminary Amendment attached hereto.  
 was filed on as Application Serial No.  
 and was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby appoint the following practitioners to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

Customer Number 24251



Please address all correspondence and telephone calls to:

Edward C. Kwok  
**SKJERVEN MORRILL MacPHERSON LLP**  
25 Metro Drive, Suite 700  
San Jose, California 95110-1349

Telephone: 408-453-9200  
Facsimile: 408-453-7979

I declare that all statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and all statements made herein are made with the knowledge that whoever, in any matter within the jurisdiction of the Patent and Trademark Office, knowingly and willfully falsifies, conceals, or covers up by any trick, scheme, or device a material fact, or makes any false, fictitious or fraudulent statements or representations, or makes or uses any false writing or document knowing the same to contain any false, fictitious or fraudulent statement or entry, shall be subject to the penalties including fine or imprisonment or both as set forth under 18 U.S.C. 1001, and that violations of this paragraph may jeopardize the validity of the application or this document, or the validity or enforceability of any patent, trademark registration, or certificate resulting therefrom.

Full name of sole (or first joint) inventor: Adi Srinivasan

Inventor's Signature:

A. Srinivasan

Date:

11/30/01

Residence: Fremont, California

Post Office Address: 35820 Killorglin Cmn.  
Fremont, California 94536-1576

Citizenship: USA

824579 v1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Srinivasan, Adi

Assignee: Sequence Design, Inc.

Title: Circuit Optimization For Minimum Path Timing Violations

Serial No.: 10/008,458 Filing Date: November 30, 2001

Docket No.: M-11985 US

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COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST**

Sir:

The above-identified assignee, hereby revokes all powers of attorney previously given and appoints the attorney(s) and/or agent(s) identified below to prosecute the above-identified application and to transact all business in the United States Patent and Trademark Office in connection therewith:

**Customer No. 32605**

Please address all correspondence and telephone calls regarding this application to:

Edward C. Kwok  
MacPherson Kwok Chen & Heid LLP  
2001 Gateway Place, Suite 195E  
San Jose, CA 95110  
Phone: (408) 392-9250  
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**ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(B)**

The undersigned representatives of the above-identified assignees certify that they are the assignees of the entire right, title and interest in the above-identified patent application/patent

**ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(B)**

The undersigned representatives of the above-identified assignees certify that they are the assignees of the entire right, title and interest in the above-identified patent application/patent by virtue of a chain of title from the inventor(s) of the above-identified patent application/patent to the above-identified assignees as shown below:

1. From: Srinivasan, Adi To: Sequence Design, Inc.

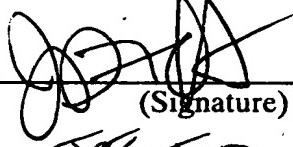
The document was recorded in the United States Patent and Trademark Office at Reel 012563, Frame 0835. A copy of the document is attached.

The document was submitted to the Patent and Trademark Office for recordation on \_\_\_\_\_ . A copy of the document is attached.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

Date: 3/19/03

SEQUENCE DESIGN, INC.

By: 

(Signature)

Name: Joe Fukukawa

(Printed or Typed Name)

Title: Secretary